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PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
851763.447APPLICATION NO.
10/776,024

APPLICANTS

Andrea Pagni et al.

FILING DATE
February 10, 2004GROUP ART UNIT
2183

U.S. PATENT DOCUMENTS

| *EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE |
|----------------------|----|-----------------|------|------|-------|----------|-------------------------------|
| | AA | | | | | | |
| | AB | | | | | | |
| | AC | | | | | | |
| | AD | | | | | | |
| | AE | | | | | | |
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| | AG | | | | | | |
| | AH | | | | | | |
| | AI | | | | | | |
| | AJ | | | | | | |

FOREIGN PATENT DOCUMENTS

| | | DOCUMENT NUMBER | DATE | COUNTRY | TRANSLATION | |
|----|----|-----------------|----------|---------|-------------|----|
| | | | | | YES | NO |
| ES | AK | 1 028 370 A2 | 08/16/00 | EP | | |
| ES | AL | WO 02/086699 A2 | 10/31/02 | WIPO | | |
| | AM | | | | | |
| | AN | | | | | |
| | AO | | | | | |

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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|----|----|------------------------------------------------------------------------------------------------------------------------------------|
| ES | AP | IBM Corp., "Real-Time CISC Architecture HW Emulator on a RISC Processor," IBM Technical Disclosure Bulletin 37(3):605, March 1994. |
| | AQ | |
| | AR | |

EXAMINER

Eun Lee

DATE CONSIDERED

4/16/06

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).